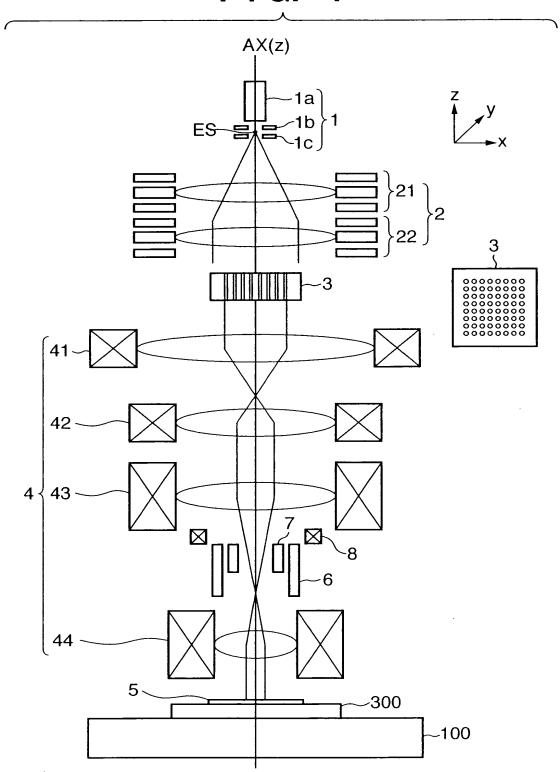
FIG. 1



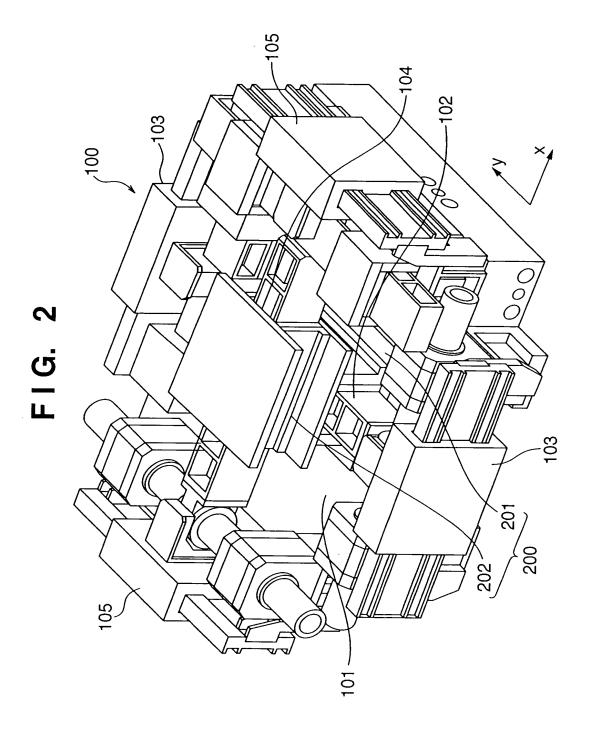


FIG. 3

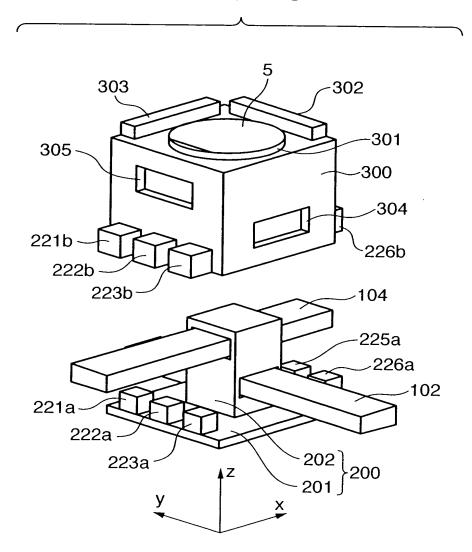


FIG. 4

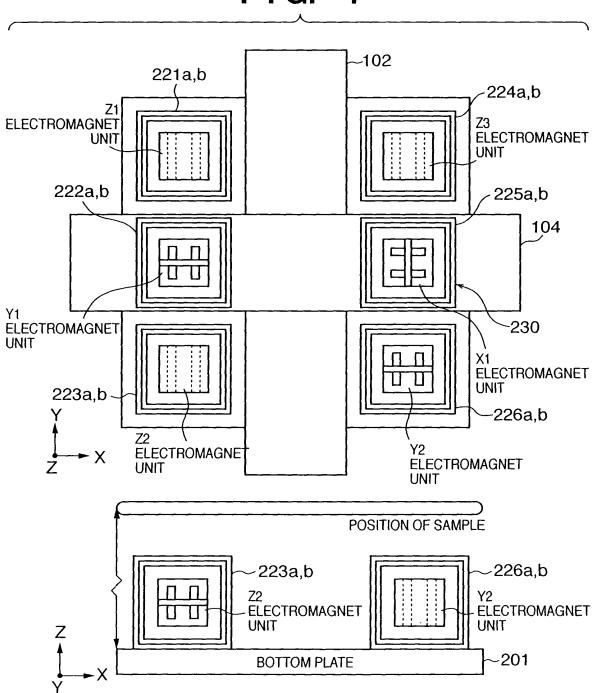
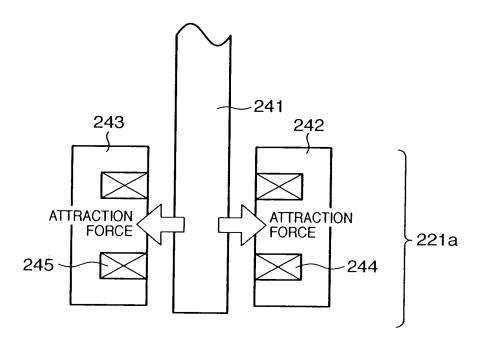
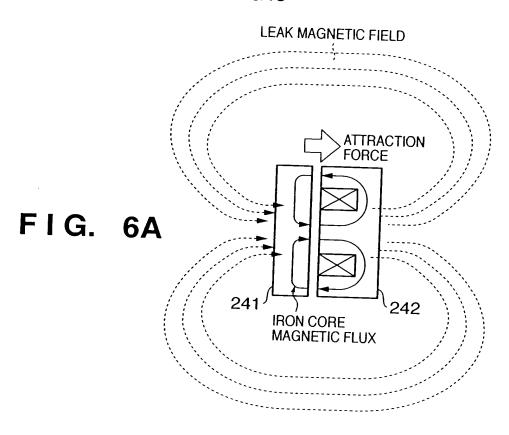
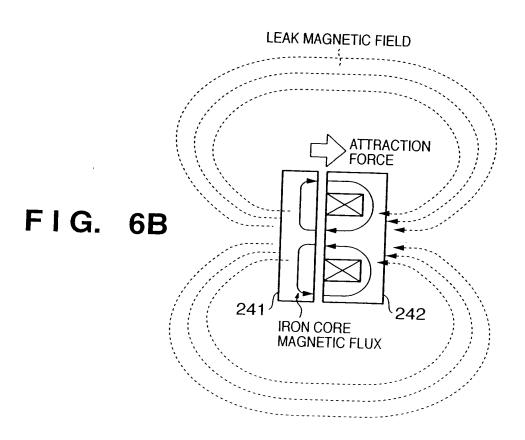


FIG. 5







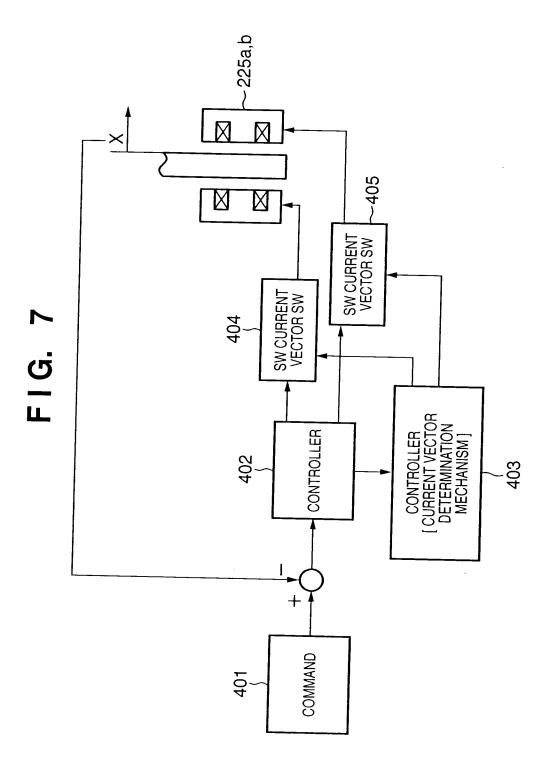
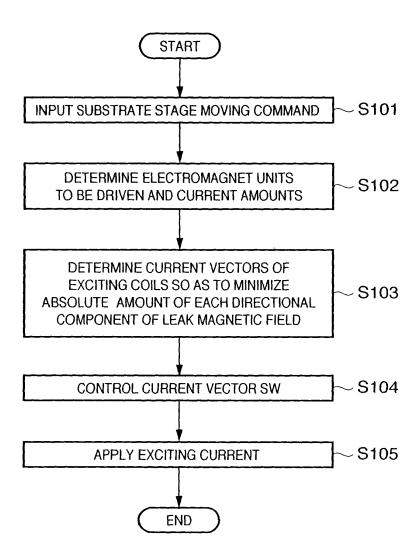
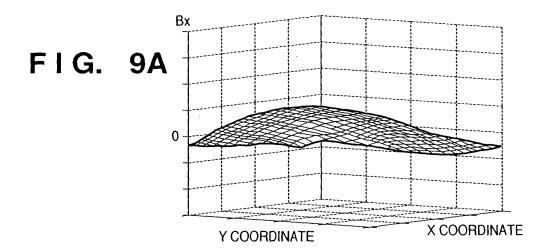
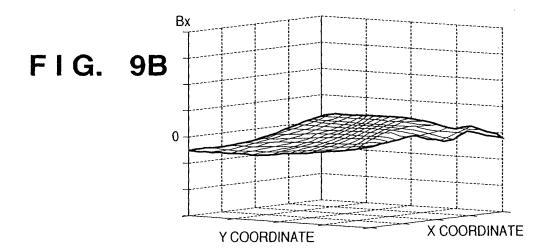
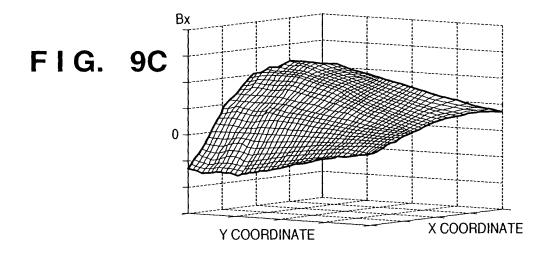


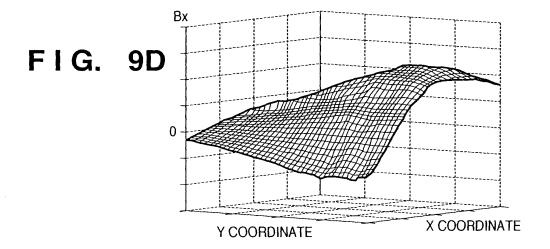
FIG. 8



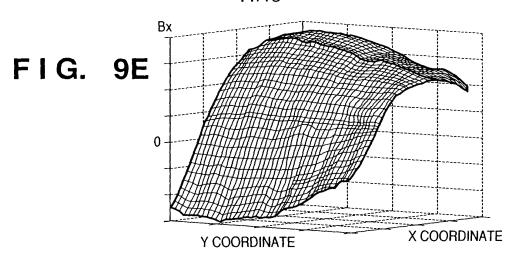


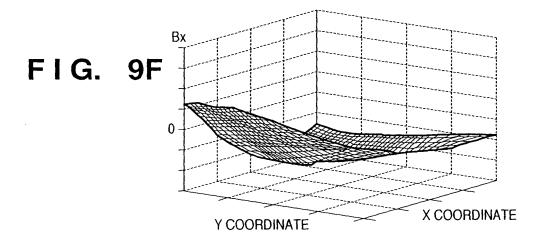












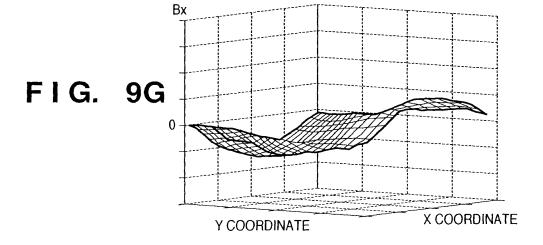


FIG. 10

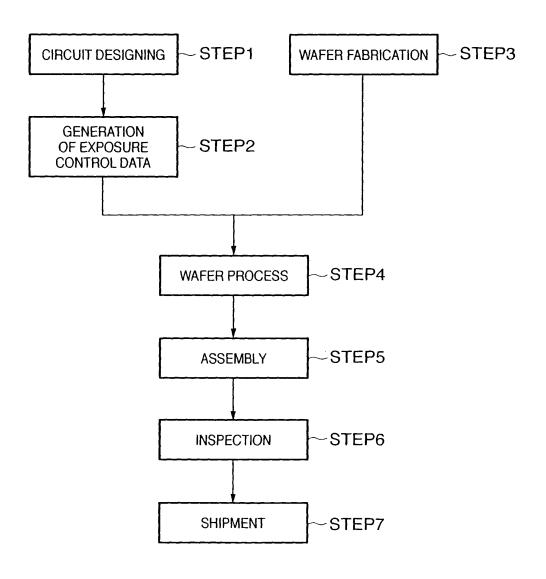
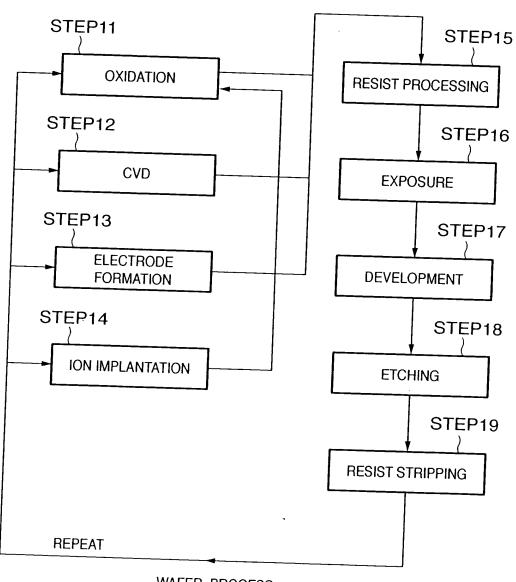


FIG. 11



WAFER PROCESS